

**Device:** USPAT;US-PCBUS;EP0;JPO;DERIVENT;IBM\_100

**Default operator:** DR

☒ Display

☒ Highlight of list items

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	O	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	R	G	A
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6480557 B1	20021112	16	Portable GPS-receiver for a personal safety system	375/349			Rog, Andrey Leonidovich et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6463109 B1	20021008	30	Multiple channel adaptive data recovery system	375/355	370/503; 375/371;		McCormack, Gary D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6271681 B1	20010807	13	PCI-compatible programmable logic devices	326/41	326/101		Cliff, Richard G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6253346 B1	20010626	8	Data transmission circuit having cyclic redundancy	714/758	375/225		Kim, Si Joong et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6218864 B1	20010417	9	Structure and method for generating a clock enable	326/93	326/31; 326/38		Young, Steven P. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5857095 A	19990105	18	Method for aligning a control signal and a clock	713/401	711/100		Jaddeloh, Joseph M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5528186 A	19960618	10	Timing generator using digital signals to obtain	327/266	327/172; 327/263;		Imamura, Makoto	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5260888 A	19931109	12	Shift and add digital signal processor	708/308			Sharman, Richard A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6369624 B1	20020409	27	Programmable phase shift circuitry	327/156	327/147; 327/150;		Wang, Bonnie I. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6286118 B1	20010904	29	Scan path circuitry including a programmable	714/726	714/724; 714/725		Churchill, Jonathan F. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 5146592 A	19920908	79	High speed image processing computer with overlapping	345/607	345/505; 345/545		Pfeiffer, David M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 20010050585 A1	20011213	14	Digital delay line with synchronous control	327/277			Carr, Larrie	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>